

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Kiyoyuki NAKAGAWA, et al.) Group Art Unit: 3723 (expected)
)
Serial No.: Unassigned) Examiner: L. Wilson (expected)
(Divisional of 09/401,526))
)
Filed: November 20, 2001)
)
For: ELECTRONIC COMPONENT CHIP)
FEEDER AND MANUFACTURING)
METHOD OF ELECTRONIC DEVICES)
USING ELECTRONIC COMPONENT)
CHIPS)

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Date: November 20, 2001

Sir:

Prior to an examination on the merits, please first amend the above-identified application as follows:

IN THE CLAIMS

Please cancel claims 1-6 without prejudice.

Please amend claims 9 and 12 and add new claims 13-17 as follows:

9. (Amended) A method according to Claim 7, wherein said step of cleaning outer surfaces of the electronic component chips comprises a process of grinding outer surfaces of electronic component chips.

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12. (Amended) A method according to Claim 7, wherein said step of cleaning outer surfaces of the electronic component chips is performed by washing outer surfaces of electronic component chips using a washing liquid.

13. (New) A method according to Claim 8, wherein said step of cleaning outer surfaces of the electronic component chips comprises a process of grinding outer surfaces of electronic component chips.

14. (New) A method according to Claim 8, wherein said step of cleaning outer surfaces of the electronic component chips is performed by washing outer surfaces of electronic component chips using a washing liquid.

15. (New) A method according to Claim 9, wherein said step of cleaning outer surfaces of the electronic component chips is performed by washing outer surfaces of electronic component chips using a washing liquid.

16. (New) A method according to Claim 10, wherein said step of cleaning outer surfaces of the electronic component chips is performed by washing outer surfaces of electronic component chips using a washing liquid

17. (New) A method according to Claim 11, wherein said step of cleaning outer surfaces of the electronic component chips is performed by washing outer surfaces of electronic component chips using a washing liquid.

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REMARKS

By the present Preliminary Amendment, claims 1-6 are cancelled without prejudice. Claims 7-12 remaining in the application were restricted in the parent application in a restriction requirement dated July 13, 2000 (Paper No. 6) and claims 13-17 have been added.

A favorable consideration on the merits is believed to be next in order, and is earnestly solicited.

If for any reason the Examiner feels that the application is not now in condition for allowance, it is respectfully requested that the Examiner contact, by telephone, the Applicants' undersigned attorney at the indicated telephone number to arrange for an interview to expedite the disposition of this case.

In the event that any additional fees are due with this paper, please charge our Deposit Account No. 02-4800.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS

By: 

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Dated: November 20, 2001

Marked-up Copy of Claims 9 and 12

9. (Amended) A method according to [any one of Claims 7 and 8] Claim 7, wherein said step of cleaning outer surfaces of the electronic component chips comprises a process of grinding outer surfaces of electronic component chips.

12. (Amended) A method according to [any one of Claims 7 to 11] Claim 7, wherein said step of cleaning outer surfaces of the electronic component chips is performed by washing outer surfaces of electronic component chips using a washing liquid.

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REQUEST FOR APPROVAL OF DRAWING CHANGES

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The Examiner's approval is respectfully requested for the proposed changes to Figures 6-8 shown in red ink on the accompanying sheets.

Upon receipt of the Examiner's approval and a Notice of Allowance, these changes will be implemented through newly submitted drawings.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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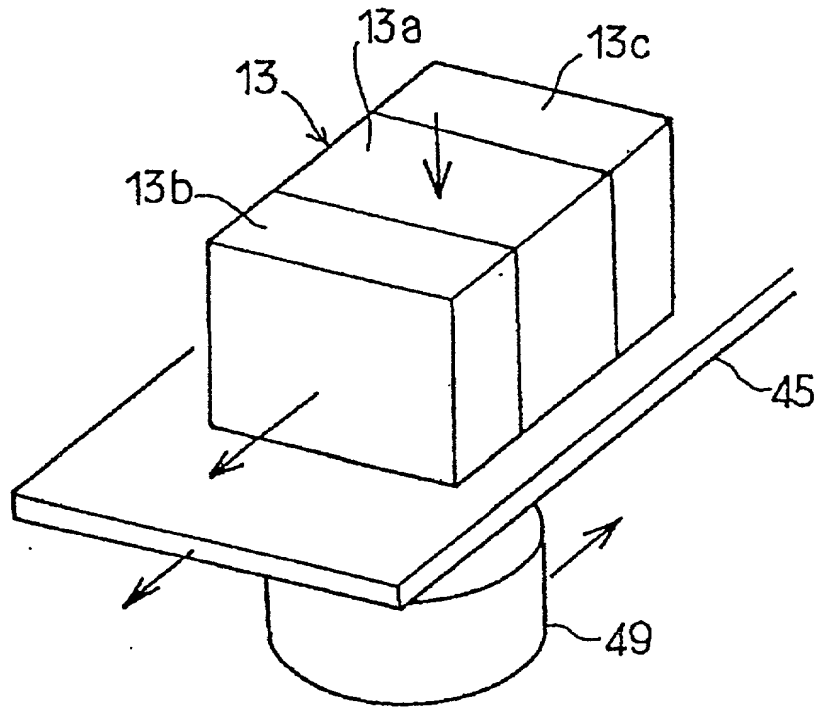
Date: November 20, 2001

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~~【図6】~~

FIG. 6

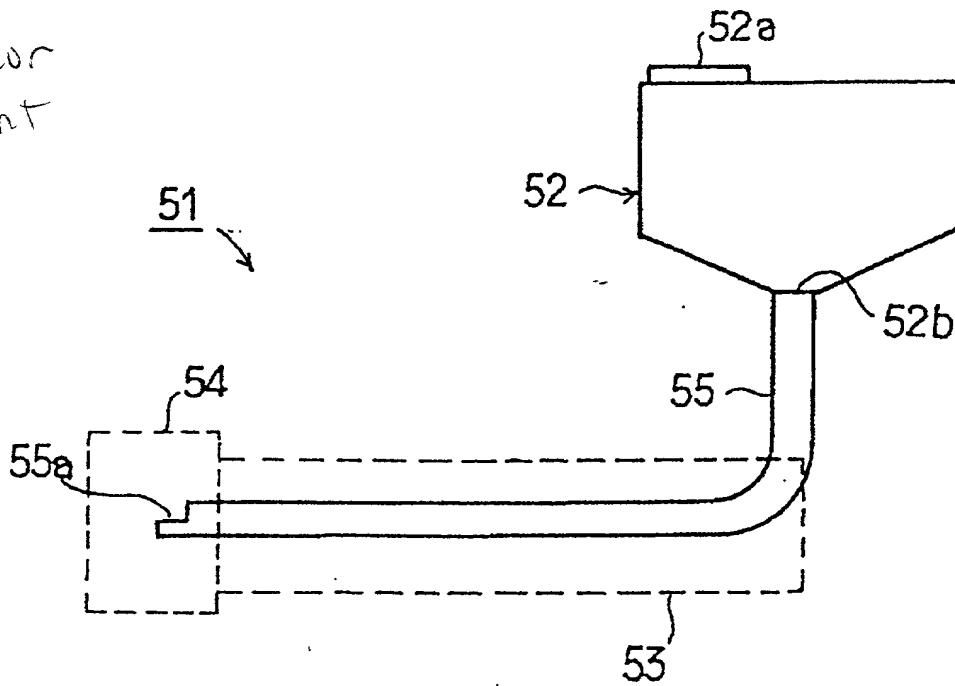
Prior
Ant



~~【図7】~~

FIG. 7

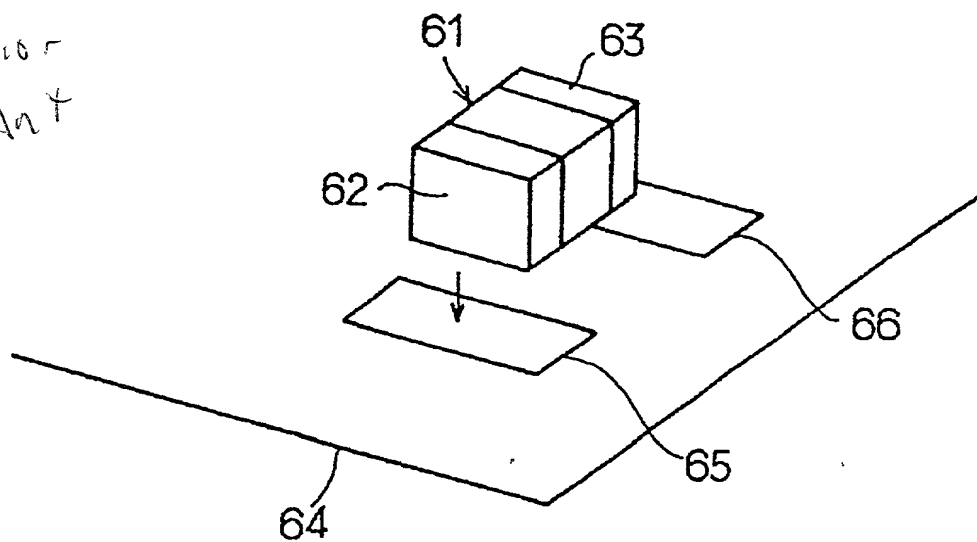
Prior
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~~【図8】~~

FIG. 8

Prior
Art



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